IN THE SPECIFICATION

Please cancel the paragraph at page 14, lines 20-28 of the specification-as-filed (i.e., the paragraph starting with "It should be appreciated that the individual blocks") and replace it with the following replacement paragraph:

It should be appreciated that the individual blocks illustrated in Fig. 10 may be functional in nature and do not necessarily correspond to discrete hardware elements. For example, in at least one embodiment, two or more of the blocks are implemented in software within a single (or multiple) digital processing device(s). The digital processing device(s) may include, for example, a general purpose microprocessor, a digital signal processor (DSP), a reduced instruction set computer (RISC), a complex instruction set computer (CISC), a field programmable gate array (FPGA), an application specific integrated circuit (ASIC), and/or others, including combinations of the above. Hardware, software, firmware, and hybrid implementations may be made. For example, in at least one software implementation, the invention is embodied as instructions stored on a storage medium that may be executed by a computing platform.